

FORM PTO-1449 U.S. Department of Commerce  
(Rev. 4/92) Patent and Trademark OfficeATTY. DOCKET NO.  
500.36179CC2SERIAL NO.  
10/639445  
Rule 53(b) of 10/028,425INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT

(Use several sheets if necessary)

APPLICANT  
NAKAGAWA et al.FILING DATE  
August 13, 2003GROUP  
Unassigned

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER							DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
HK	4	7	7	9	1	9	2	10/88	TORII et al.	712	8	
	5	2	0	3	0	0	3	4/93	DONNER	713	322	
	6	2	3	0	2	5	5	5/01	ASGHAR et al.	712	32	
	5	2	9	3	5	8	6	3/94	YAMAZAKI et al.	345	519	
	5	3	1	5	7	0	0	5/94	JOHNSTON et al.	345	502	
	5	7	9	4	0	6	7	8/98	KADOWAKI	712	35	
	6	3	5	3	8	6	3	3/02	NAKAGAWA et al.	710	5	
	5	3	1	1	4	5	8	5/94	COVEY	708	603	
	5	5	1	9	8	7	9	5/96	CARMON	712	35	
HK	5	4	9	1	8	2	8	2/96	AFEK	712	35	

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER							DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
													YES	
HK		5	1	2	8	2	8	0	5/93	JP				
		4	2	1	9	0	4	5	8/92	JP				
		4	1	6	7	1	6	8	6/92	JP				
		3	0	8	0	6	8	9	8/91	JP				
		2	2	9	7	5	9	4	12/90	JP				
HK		6	0	2	0	4	0	2	9	10/85	JP			

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

HK	1	"DSP56116 Digital Signal Processor User User's Manual" Issued by Motorola Inc., pp. 1-1 to 5-16, 1990.
HK	2	Nikkei Electronics Vol. 463, December 26, 1998 (Nikkei BP Corp.), Osamu Kobayashi and other, "DSP which spreads its application scope on the full-scale debut of 32-bit floating points" pp. 133-146, Figs. 5, 12.
HK	3	"Latest Information on GSM/Systems, Terminals and Services", Seminar materials, Japan Industrial Technological Center, May 18 and 19, 1995 and "Development Trend of GSM telephone Terminal Devices", pp. 118-130, Japan Phillips.

EXAMINER *Harold Q.*

DATE CONSIDERED

8/22/2005

EXAMINER: Initial if citation is considered, draw line through citation if not in current and not considered. Include copy of this form with next communication to applicant.

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(Rev. 4/92) Patent and Trademark OfficeATTY. DOCKET NO.  
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## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER							DATE	NAME	CLASS	SUBCLAS S	FILING DATE IF APPROPRIATE
NK	6	0	4	7	3	5	8	4/00	JACOBS	711	133	
	5	3	9	2	4	4	8	2/95	FRANKEL et al.	712	35	
	4	7	9	1	5	9	0	12/98	KU et al.	708	404	
	5	5	6	7	9	0	0	10/96	HIGASHI	84	602	
	5	5	9	2	6	7	7	1/97	INTRATER et al.	712	34	
	5	6	3	0	1	5	3	5/97	INTRATER et al.	712	35	
	5	7	6	8	6	1	3	6/98	ASGHAR	712	35	
	5	8	6	7	7	2	6	2/99	OHSUGA et al.	712	32	
	5	8	8	4	0	9	2	3/99	KIUCHI et al.	712	35	
NK	5	3	1	1	4	5	8	5/94	HAINES et al.			

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLAS S	TRANSLATION	
							YES	NO
NK	0	0 9 2 2 3 7 9	1/97	JP				
	0	4 4 2 0 4 1	8/91	EP				
	0	4 6 5 0 5 4	1/92	EP				
	40	7 0 1 3 5 6 1	1/95	JP				
NK	9	7 1 4 0 9 3	4/97	WO				

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

NK	4	"Hitachi Single-Chip RISC Microcomputers SH7032, SH7034 Hardware Manual", third edition, March 1964, issued by Hitachi, Ltd.
NK	5	"A diversified application study for memory IC" Interface No. 123, August 1987 (CQ Publishing).
NK	6	"Super RISC Engine SH7604 Hardware Manual", First Edition, September 1994, issued by Hitachi, Ltd.

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*Harold G.*

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ATTY. DOCKET NO.  
**500.36179CX1**

SERIAL NO. 10/634  
Rule 53(b) of 10/028.425

## **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

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**APPLICANT**  
**NAKAGAWA et al.**

**FILING DATE**  
**August 13, 2003**

GROUP  
Unassigned

## **U.S. PATENT DOCUMENTS**

## FOREIGN PATENT DOCUMENTS

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

HK	7	W. Patrick Hays, "A Programmable Digital Signal Processor with 32b Floating Point Arithmetic", 1985 IEEE International Solid-State Circuits Conference DIGEST OF TECHNICAL PAPERS, pp. 92-93, 318
NLC	8	R. Meyer, et al., Convolution Algorithms on DSP Processors, 1991, pp. 2193-2196.

**EXAMINER**

Harold G.

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6/22/05

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SHEET 1 OF 1

<b>FORM PTO-1449</b> U.S. Department of Commerce (Rev. 4/92) Patent and Trademark Office	<b>ATTY. DOCKET NO.</b>	<b>SERIAL NO.</b>
	<b>500.36179CC2</b>	<b>10/639,445</b>
<b>APPLICANT</b> <b>Tetsuya NAKAGAWA et al.</b>		
<b>FILING DATE</b> <b>August 13, 2003</b>		<b>GROUP</b> <b>Unassigned</b>
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>		
<small>(Use several sheets if necessary)</small>		

## **U.S. PATENT DOCUMENTS**

## FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	ABSTRACT	
						YES	NO
WZ	JP 7-38509	2-1995	Japan				
	JP 7-84963	3-1995	Japan			X	
	JP 61-150061	7-1986	Japan			X	
WZ	JP 5-81219 (w/translation)	4-1993	Japan				

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

"Exploration of Multimedia Trend and Strategies Underlying SH7600 series", pp. 32-33, Nikkei Electronics, May 23, 1994, Vol. 608, Nikkei Business Publications, Inc. (with translation)

EXAMINER *Harold F.*

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6/22/2005

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(Form PTO-1449 [6-4])